



UNITED STATES PATENT AND TRADEMARK OFFICE

| In Re Application of |) | |
|---------------------------------------|---------------|---|
| Fujimoto et al. |) | I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: |
| Serial No.: 10/017,114 |) | .Commissioner for Patents , |
| Filed: October 24, 2001 | $\frac{1}{2}$ | P.O. Box 1450, Alexandria, VA 22313-1450 on November 4, 2003 |
| 1 ned. October 24, 2001 | | (Date of Deposit) |
| For: Semiconductor Device And Metho | od of | Shannon Wallace |
| Manufacturing-The Same | -) | Name of applicant assignee, or Registered Rep. |
| |) [| Signature Date |
| Art Unit: 2818 |) [| |
| · · · · · · · · · · · · · · · · · · · |) | |
| Examiner: Phuc Dang |) | |
| Commissioner for Patents | | ECHNO R |
| PFO Box 1450% 20202 | | 10 6 A |

Alexandria, VA 22313-1450 200 West Adems Street. CLAMINISES & WEALER, LIFO.

Sir:

In response to the Restriction Requirement of October 10, 2003, Applicants elect Group I, Claims 1-21 and 24-50 in the above-identified application. Applicants are making this election without prejudice to later filing a divisional application on the non-elected claims.

Respectfully submitted,

Date: November 4, 2003

Mark J. Murphy

Registration No. 34,225

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